

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Andrew Dellow et al.  
Application No. : 10/583,577  
Filed : July 2, 2007  
For : MONOLITHIC SEMICONDUCTOR INTEGRATED CIRCUIT  
AND METHOD FOR SELECTIVE MEMORY ENCRYPTION  
AND DECRYPTION

Examiner : Shin-Hon Chen  
Art Unit : 2431  
Docket No. : 851963.425USPC  
Date : February 11, 2010

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

FEE DEFICIENCY AUTHORIZATION FORM

Commissioner for Patents:

Applicants hereby authorize the Director to charge any deficiencies in fees due by way of the enclosed papers only under 37 CFR 41.20(b) and 1.17 to Deposit Account No. 19-1090.

Respectfully submitted,  
SEED Intellectual Property Law Group PLLC

/Timothy L. Boller/  
Timothy L. Boller  
Registration No. 47,435

701 Fifth Avenue, Suite 5400  
Seattle, Washington 98104  
Phone: (206) 622-4900  
Fax: (206) 682-6031

1561361\_1